

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S3	2	"20020193972"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/22 13:36
S4	2	"5590268".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/22 13:36
S5	22	("4375674" "4970666" "5138698" "5276785" "5305429" "5310349" "5333257" "5339390" "5432895").PN. OR ("5590268"). URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/07/22 13:42
S6	125	(work same cell same simulation)	US-PGPUB; USPAT; USOCR	OR	OFF	2005/07/22 13:43
S7	38	(work same cell same simulation) not circuit	US-PGPUB; USPAT; USOCR	OR	OFF	2005/07/22 13:43
S8	6	("5357439" "5745735" "5949693" "6004016" "6292715" "6470301").PN. OR ("6847922").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/07/22 13:45
S9	38	(work same cell same simulation) not circuit	US-PGPUB; USPAT; USOCR	OR	OFF	2005/08/01 16:50
S10	1	S9 and verification	US-PGPUB; USPAT; USOCR	OR	OFF	2005/08/01 17:07
S13	1	09/993715	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/02 09:21
S14	1	"09/993715"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/02 12:58
S15	672	700/97.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/02 12:58



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... 1, 1994, pp.39-46. Michael J. Griesmeyer and Fred J. Oppel. "Process Subsystem Architecture for Virtual Manufacturing Validation ...

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... 1, pp. 39-46, 1994. [2] MJ Griesmeyer and FJ Oppel, "Process Subsystem Architecture for Virtual Manufacturing Validation," Proceedings 223 ...

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